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By:

Ralph E. Locher

Date:

Aug 16, 2007

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/730,619 Conf. No.: 4277  
Applicant : Burkhard Becker  
Filed : December 8, 2003  
Art Unit : 2185  
Examiner : Yong Choe  
Title : Method and Circuit Configuration for Transmitting Data Between a Processor and a Hardware Arithmetic-Logic Unit  
Docket No. : L&L-I0225  
Customer No. : 24131

A M E N D M E N T under 37 C.F.R. §1.116

Mail Stop Box AF Amendment  
Hon. Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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08/29/07  


Sir:

Responsive to the final Office Action dated June 18, 2007

kindly amend the application as follows: